

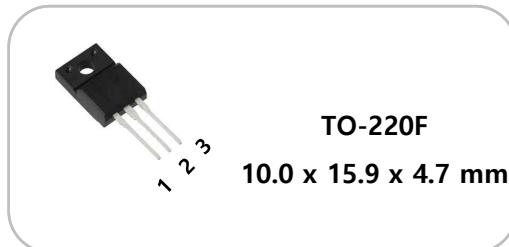


### General description

Symbol	Value
$V_{DSS}$ @ $T_c=25^\circ\text{C}$	Min 650V
$I_D$ @ $T_c=25^\circ\text{C}$	20A
$R_{DS(on)}$	Max 200mΩ
$Q_g$	Typ 35nC

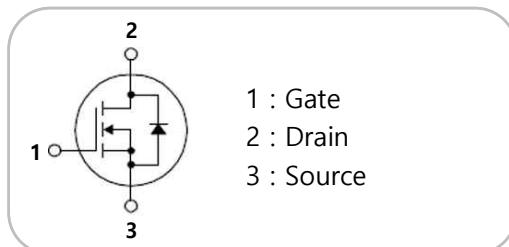


### Package



### Features

- Gate Charge(Typ.  $Q_g=35$  nC)
- Improved dv/dt Capability
- 100% Avalanche Tested



### Applications

- LCD/LED/PDP TV
- Telecom/Server Power supplies
- AC-DC Power Supply
- LED Lighting



### Maximum Ratings ( $T_c = 25^\circ\text{C}$ )

Parameter	Symbol	Test Condition	Value	Units
Drain - source voltage	$V_{DSS}$	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	650	V
Drain current	$I_D$	$T_c=25^\circ\text{C}$	20	A
Drain current	$I_{DM}$	Pulse width limited by junction temperature	40	A
Gate-source voltage	$V_{GS}$	-	$\pm 30$	V
Single pulsed avalanche energy	$E_{AS}$	$I_{AS}=9.0\text{A}$ , $R_G=25\Omega$ , $V_{DD}=50\text{V}$ , $L=20\text{mH}$	810	mJ
Power dissipation	$P_D$	$T_c=25^\circ\text{C}$	33	W
Operating junction	$T_j$	-	-55 to 150	°C
Storage temperature	$T_{stg}$	-	-55 to 150	°C




**Electrical Characteristics ( $T_j = 25^\circ\text{C}$ )**

Parameter	Symbol	Test Condition	Value			Units
			Min	Typ	Max	
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=1\text{mA}$	650	-	-	V
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}}=650\text{V}, V_{\text{GS}}=0\text{V}$	-	-	30	uA
Gate-source leakage current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 30\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 70$	nA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\text{uA}$	3	-	5	V
Drain-source on-state resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	-	190	200	mΩ
Gate input resistance	$R_{\text{G}}$	$V_{\text{GS}}=0\text{V}, f=100\text{kHz}$	-	4	5	Ω
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=380\text{V}, V_{\text{GS}}=0\text{V}$ $f = 100\text{kHz}$	-	2000	-	pF
Output capacitance	$C_{\text{oss}}$		-	1500	-	
Reverse transfer capacitance	$C_{\text{rss}}$		-	20	-	
Total gate charge at 10V	$Q_{\text{G}(\text{tot})}$	$V_{\text{DS}}=380\text{V}, I_{\text{D}}=10\text{A},$ $V_{\text{GS}(\text{on})}=10\text{V}, V_{\text{GS}(\text{off})}=0\text{V}$	-	35	-	nC
Gate-source charge	$Q_{\text{GS}}$		-	10	-	
Gate-drain charge	$Q_{\text{GD}}$		-	20	-	
Turn on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=380\text{V}, I_{\text{D}}=10\text{A},$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=4.7\Omega$	-	18	-	ns
Rise time	$t_{\text{r}}$		-	20	-	
Turn off delay time	$t_{\text{d}(\text{off})}$		-	64	-	
Fall time	$t_{\text{f}}$		-	21	-	

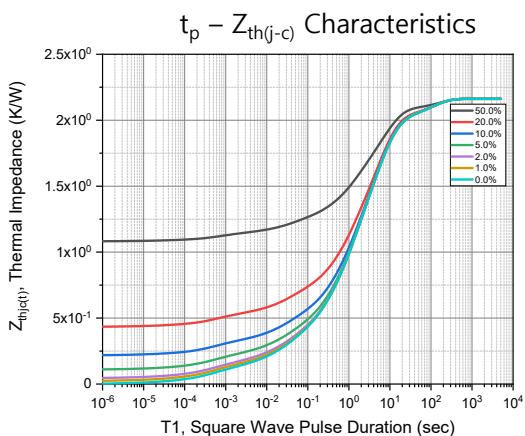


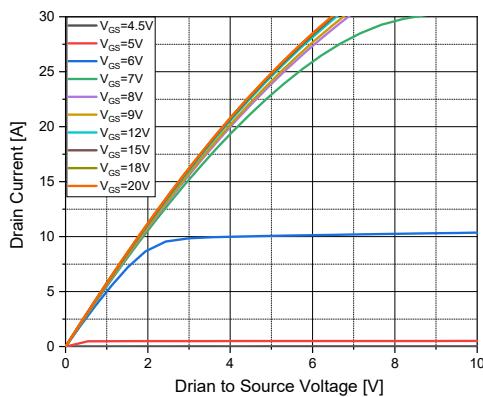
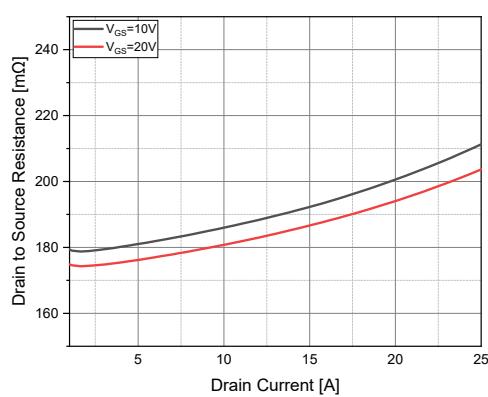
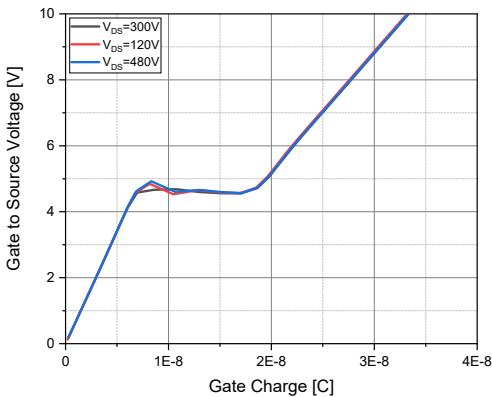
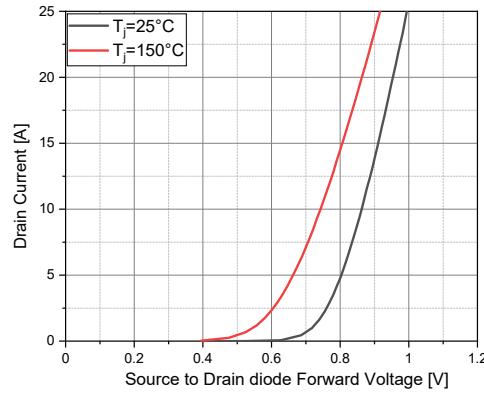
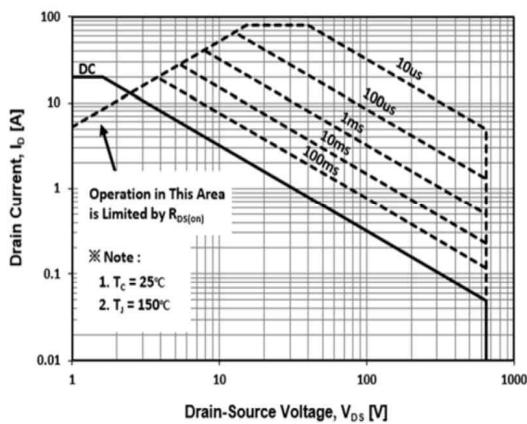
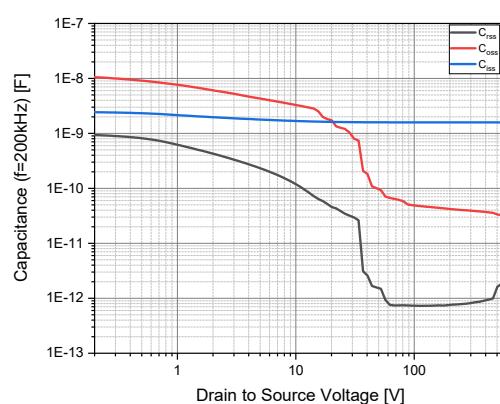

**Body Diode(Source – Drain) Electrical Characteristics (T<sub>j</sub> = 25°C)**

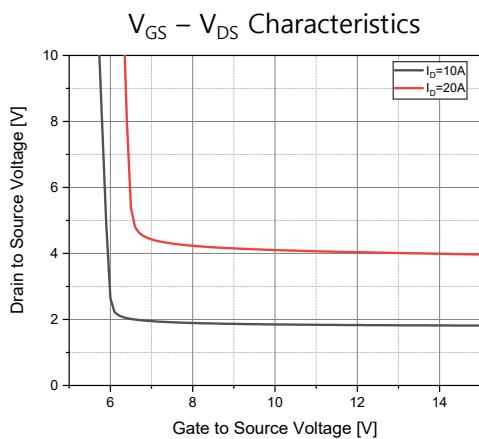
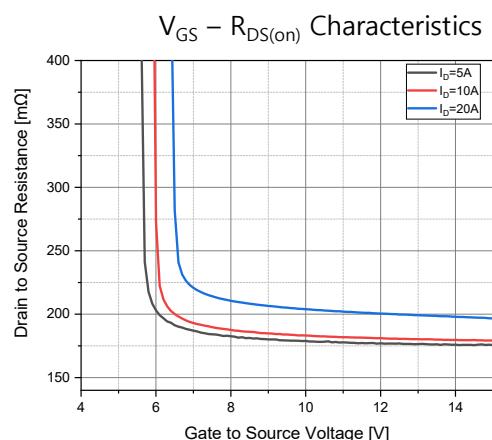
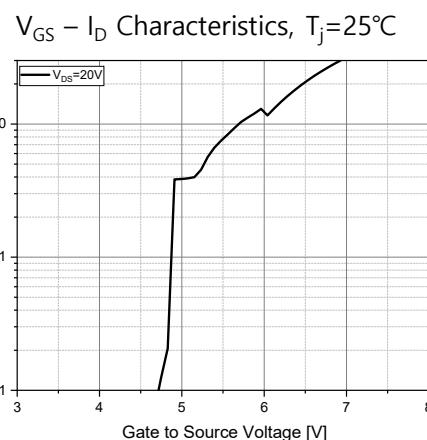
Parameter	Symbol	Test Condition	Value			Units
			Min	Typ	Max	
Continuous diode forward current	I <sub>S</sub>	-	-	-	20.0	A
Maximum pulsed drain to source diode forward current	I <sub>SM</sub>	-	-	-	80.0	
Forward voltage	V <sub>SD</sub>	I <sub>SD</sub> =10A, V <sub>GS</sub> =0V	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub>	I <sub>SD</sub> =10A, V <sub>GS</sub> =0V di/dt=100A/μs	-	330.0	-	ns
Reverse recovery charge	Q <sub>rr</sub>		-	4.8	-	nC
Peak reverse recovery current	I <sub>rrm</sub>		-	29.0	-	A


**Thermal Characteristics(T<sub>c</sub> = 25°C)**

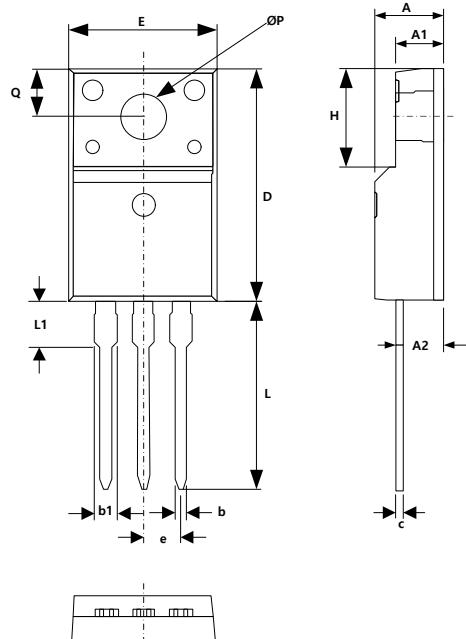
Symbol	Parameter	Typ	Max	Units
R <sub>th(j-c)</sub>	Junction to case	3.75	-	°C/W




**Typical Electrical Characteristics Curves ( $T_j = 25^\circ\text{C}$ )**
 **$V_{DS} - I_D$  Characteristics,  $T_j=25^\circ\text{C}$** 

 **$I_D - R_{DS(on)}$  Characteristics**

 **$Q_g - V_{GS}$  Characteristics**

 **$V_{SD} - I_D$  Characteristics**

**Safe Operating Area,  $T_C=25^\circ\text{C}$** 

 **$V_{DS} - C$  Characteristics**



**Typical Electrical Characteristics Curves ( $T_j = 25^\circ\text{C}$ )**


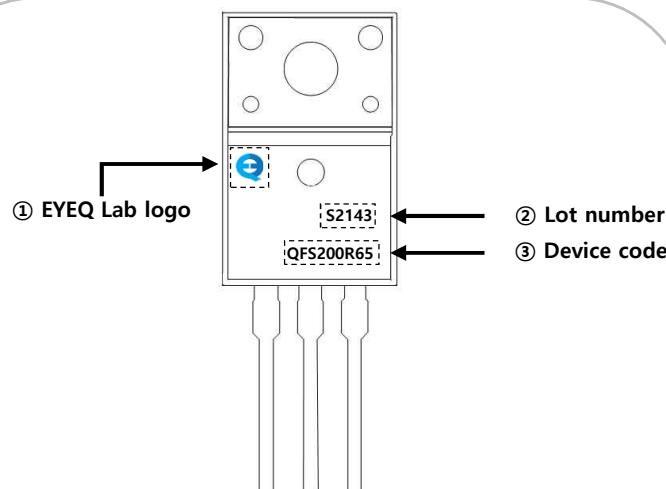
### Package Dimensions(TO-220F)



[Unit : mm]

SYMBOL	MIN	MAX
A	4.50	4.90
A1	2.34	2.74
A2	2.56	2.96
b	0.70	0.90
b1	1.27	1.47
c	0.45	0.60
D	15.67	16.07
E	9.96	10.36
e	2.54 BSC	
H	6.48	6.88
L	12.68	13.28
L1	3.03	3.43
φP	3.08	3.28
Q	3.20	3.40
A	4.50	4.90
A1	2.34	2.74
A2	2.56	2.96
b	0.70	0.90

### Marking Information



- ① Company Logo
- ② Lot Number : 21 – 2021 Year , 43 – 43 Week
- ③ Device Code : Q – EYEQ Lab, F – MOSFET, S – Super junction, 200R – 200mΩ, 65 – 650V

